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**METHOD AND APPARATUS FOR A HIGH SPEED CYCLICAL
REDUNDANCY CHECK SYSTEM**

Abstract

5 A high speed cyclical redundancy check system for use in digital
systems. The high speed cyclical redundancy check system providing
programmable error correction functions for different data protocols. The high
speed cyclical redundancy check system providing programmable data paths for
minimizing overhead and maximizing throughput. The system supporting
10 multiple operations in a single cycle.

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